Sharp Programmable Controller Board PC J-board Z-300 series

Model name

Serial Interface board : Z- 354J

User's Manual

Thank you for purchasing our J-board: Z-300 series programmable controller. This manual describes specifications and discusses the use of the serial interface board.

See the "J-board Z-300 series Z-311J/312J, Z-321J: User's Manual: Hardware Version" for installation methods and basic application of this board.

Note
 Should you have any questions or inquires, please feel free to contact one of our dealers, or or service department. Copying the whole or part of this manual is prohibited. The contents of this manual may be revised without notice.

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Safety Precautions

Read this user's manual and attached documents carefully before installation, operation, maintenance and checking in order to use this board correctly. Understand all of this board's knowledge, safety information, and cautions before starting to use. In this user's manual, safety precautions are ranked into "Danger" and "Caution" as follows.



: Wrong handling may possibly lead to death or heavy injury.

: Wrong handling may possibly lead to medium or light injury or loss on properties.

Even in the case of Δ Caution, a serious result may be experienced depending on the circumstances. Anyway, important points are mentioned. Be sure to observe them strictly.

The picture signs of prohibit and compel are explained below.

igvee : It means a don't. For example, prohibition of disassembly is indicated as (igvee).



: It means a must. For example, obligation of grounding is indicated as (🔔).

1) Installation

- Use in the environments specified in the catalog and user's manual.
 Electric shock, fire or malfunction may be caused when used in the environments of high
- temperature, high humidity, dusty or corrosive atmosphere, vibration or impact. • Install according to the instruction manual and user's manual.
- Wrong installation may cause a drop, trouble or malfunction.
- Never admit wire chips or foreign matters. Or fire, trouble or malfunction may be caused.

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2) Wiring

- Compel
 Be sure to ground.
 Unless grounded, electric shock or malfunction may be caused.
 - Caution
 - Wiring should be done by a qualified electrician. Wrong wiring may lead to fire, trouble or electric shock.

3) Use

Danger

- Never touch the terminals while electric power is charged. Otherwise, you may have an electric shock.
- Assemble an emergency stop circuit and interlock circuit outside of the J-board and integrate the J-board's halt output. Otherwise a machine breakdown or accident may be caused by the trouble of the programmable controller.

A Caution

- Manipulation for program change, forced output, RUN or STOP during operation should be done with particular care by confirming safety. Mis-operation may lead to a machine trouble or accident.
- Follow the power input order specified. Otherwise, the J-board malfunctions and damages machines or cause an accident.

4) Maintenance



• Don't disassemble or modify. Or fire, trouble or malfunction may be caused.

▲ Caution

• Make sure to turn OFF the power before removing / installing the board, installing the connectors, or changing the switch settings.

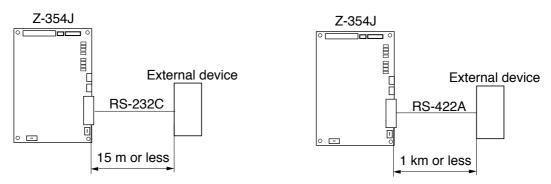
Chapter 1: Outline

The Z-354J serial interface board (hereafter referred to as "the board") is a special I/O board used to communicate with external devices. It has RS-232C/422A serial interfaces for communication with a personal computer and a bar code reader.

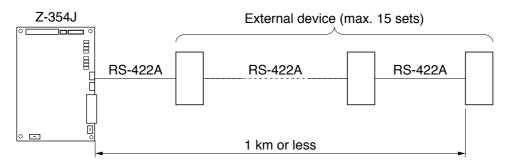
Compatible motherboards	Z-311J
	Z-312J
	Z-313J

Features and functions

1) Either RS-232C or RS-422A is selectable as communication mode.



2) When RS-422A is applied, max. 15 sets of external devices are connectable.



3) Applicable to various data formats for exchanging data with external device.

4) Max. 512 bytes of data sending/receiving is possible.

5) Outputs error information by error codes.

Chapter 2: Handling Precautions

(1) Precautions for installation

Never install the board in the following locations.

- \cdot Where proximate to any heat generating object.
- \cdot Where temperature rapidly changes and gives dew condensation.
- \cdot Where there is flammable gas.
- \cdot Where it directly receives vibration or shocks.
- \cdot Where exposed to dust, iron powder, or salty atmosphere.

(In conditions where the printed circuit boards may be directly affected by these causes, make sure to provide an appropriate external case to cover the J-board.)

• Where it is proximate to high voltage equipment, driving power tools, large open/close surge generating devices, and their wirings.

(2) Precautions at use

- In order to prevent personal injury or damage to equipment, construct an external emergency stop circuit and connect it to the halt output on the J-board.
- \cdot As J-board is board structure and it's electronic parts are exposed, be careful when handling.
- 1) Before you touch the board directly, be sure to eliminate static electricity in your body.
- 2) Do not touch directly with dirty hands such as stacked oil etc.
- Do not put the board alone directly on conductive objects such as metallic boards. (Once the J-board is installed on a motherboard, if it contacts a conductive object the battery on the motherboard may be short circuited and the back up memory may be damaged.)
- 4) Be careful not to apply excessive force to each switch, connector, terminal block of the J-board.
- \cdot Make sure to turn OFF the power before connecting the board, installing connector, changing a switch setting.

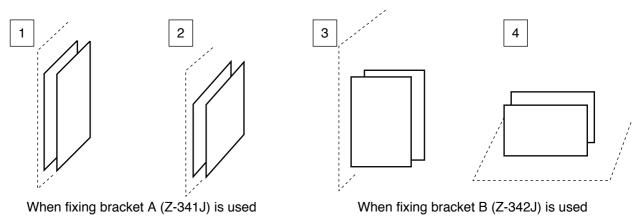
(3) Precautions for wiring

Do not run the communication lines parallel to or near to power lines or high voltage lines. Do not arrange wiring so that sources of electrical noise (driver lines, high voltage lines, or I/O lines) come close to the PC board on the J-board.

(4) Installation

Use bracket A or B (sold separately) to install the J-board in any of the following 4 positions.

[Recommending installation positions]

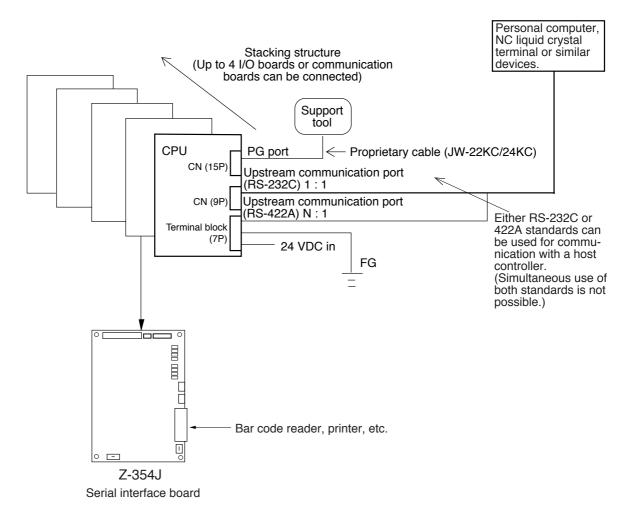


If you do not use bracket A or B to install the J-board, make sure it is very secure. Also, install the J-board with as much ventilation as possible.

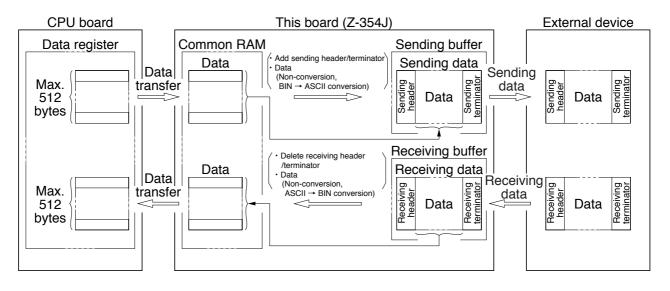
If the J-board is installed horizontally, as shown in the figure below, it will not be well ventilated. Make sure the installation allows enough space above the board so that the surrounding temperature does not rise above 55° C.

Not good ventilation

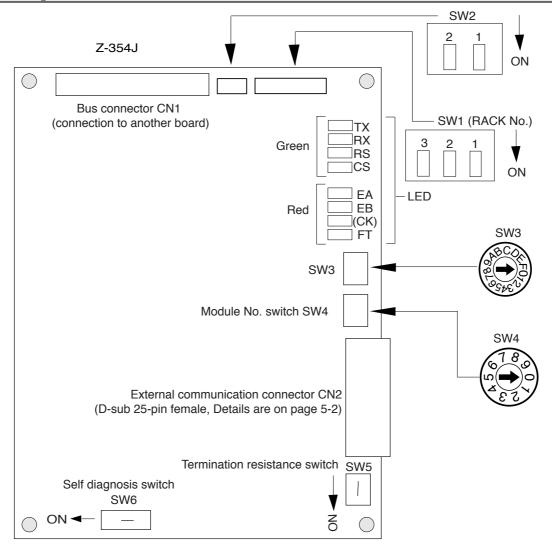
Chapter 3: System Configuration



Data flow chart



Chapter 4: Name and Function of Each Part



- Setting switches

Switch nan	ne	Switch type	Setting when delivered	Setting details	
	1		OFF		
SW1	2	Dip switch (3 poles)	OFF	Specify a rack no. Factory setting: Rack address 0	
	3		OFF		
SW2	1	Dip owitch (2 poloc)	ON	Lies it with the factory actting (ON)	
5002	2	Dip switch (2 poles)	ON	Use it with the factory setting (ON).	
SW3		Rotary switch (0 to F)	0	Use it with the factory setting (0).	
SW4 (Module switch)	No.	Rotary switch (0 to 9)	0	Specify a parameter area and a control relay area.	
	SW5 (Termination Slide switch (1 pole) ON ("ON" enables a termination resistance		("ON" enables a termination resistance, and "OFF" removes the termination resistance)		
SW6 (Self diagnosis switch)		Slide switch (1 pole)	OFF	The setting for this switch must not be changed. Keep the factory settings. (ON enables the self diagnosis)	

- LED display

LED	Indication color	Display details
TX	Green	Is ON while sending date (J-board -> external device).
RX	Green	Is ON while receiving date (J-board <- external device).
RS	Green	Is ON when the J-board is requesting an external device to send data (the J- board is ready to receive).
CS	Green	Is ON when the J-board can send data to an external device (waiting for an external device that is ready to receive).
EA	Red	Goes ON when any of the parameters or control relay settings are out of range.
EB	Red	Goes ON when a communication error occurs, such as parity error, or time out.
(CK)	Red	On during self diagnosis (goes OFF in normal use).
FT	Red	Goes ON when a hardware error occurs on the J-board.

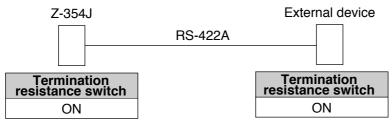
- Setting of SW5 (termination resistance switch)

When communication mode is RS-422 (2-wire system, 4-wire system), set termination resistance. At RS-232C, this setting is not required.

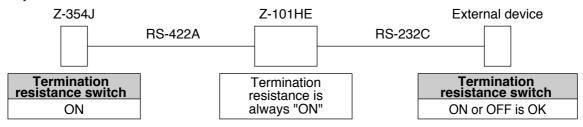
Setting	Condition of the board		
ON	Insert termination resistance		
OFF	Do not insert termination resistance		

("1" by "1" connection)

• Turn "ON" the switch on both of the module and external device.

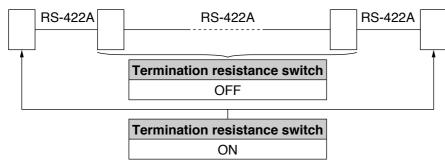


• When Z-101HE (RS-232C/RS-422A converter) is using, termination resistance of Z-101HE is always "ON."



("1" by "N" connection)

• Turn "ON" both of end stations, turn "OFF" intermediate stations.



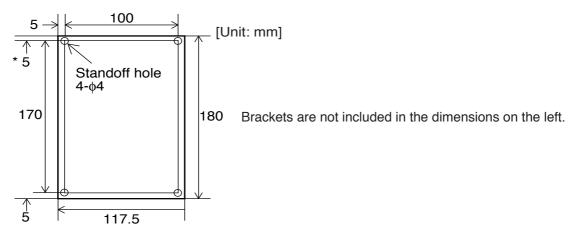
Thisº board need not necessarily be end station.

Chapter 5: Installation/Wiring Method

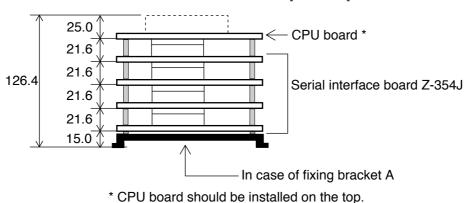
5-1. Installation method

Shown below are the PC board dimensions and assembled dimensions of the Z-354J.

- PC board dimensions



- Assembly dimensions (when 4 sets of the Z-354J are stacked)



[Unit: mm]

For the details about assembling and installing the board, see the "J-board Z-311J/312J, User's Manual, Hardware Version."

5-2. Wiring method

This board can be connected to either an RS-232C or RS-422A (2-wire system, 4-wire system) device through external communication connector CN2.

Internal circuit diagram of this board Communication connector (D-sub25P) \in SD FG FG 10 RS-232C ∕∩14 2 SD ¢ SD 20 - RD 0 015 3 RD¢ RD 3 🔿 9 RS-422 **016** 4 RS RS-232C \in RS RS 40 ()17 5 CS CS 5 🔿 ◯18 60 > cs)19 SG 70 SG **20** 2-wire system 80 **O**21 9 🔿 $\bigcirc 22$ √4-wire system 10,22 SD(+) 10 SD(+) ¢ **23** 11,23 SD(-) 11C SD(-) **2**4 RS-422 12,24 RD(+) 12 RD(+) ¢ 025 13,25 RD(-) 13 RD(-)

[1] Pin assignment of CN2 external communication connector

Std.	Pin No.	Signal name	Function	Signal direction	
	1	FG	Frame ground	—	
	2	SD	Sending data	Z-354J -> External device	
	3	RD	Receiving data	Z-354J <- External device	
RS-232C	4	RS	Request to send	Z-354J -> External device	
	5	CS	Ready to send	Z-354J <- External device	
	6		Not used		
	7	SG	Signal ground		
	8, 9		Not used —		
	10,22	SD (+)	Conding signal		
RS-422A	11,23	SD (–)	Sending signal	Z-354J -> External device	
no-422A	12,24	RD (+)	Desciving signal	Z-354J <- External device	
	13,25	RD (–)	Receiving signal		
	14 to 21		Not used		

[2] Connection example

(1) RS-232C (In case of control signal: absent, XON/XOFF manual, or XON/XOFF automatic)

Z-3	54J	Shield cable E	xternal device
Pin No.	Signal name		Signal name
1	FG	<u> </u>	FG
2	SD		SD
3	RD		RD
7	SG	/	SG
		15 m or less	-

RS and CS of external device might be shorted in accordance with specification (available operation when both RS and CS are "ON") of its external device.

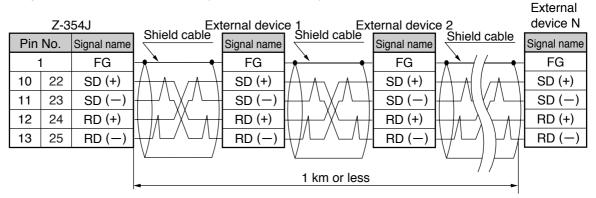
Some external device cannot communicate unless DCD (carrier detection) is "ON." In this case, latch DCD terminal signal at the external device side or loop back "ON" voltage signal.

Z-354J Shield cable External device Pin No. Signal name Signal name FG 1 FG 2 SD SD 3 RD RD 4 RS RS 5 CS CS 7 SG SG 15 m or less

(2) RS-232C (In case of control signal: RS/CS manual, or RS/CS automatic)

(3) RS-422A (4-wire system)

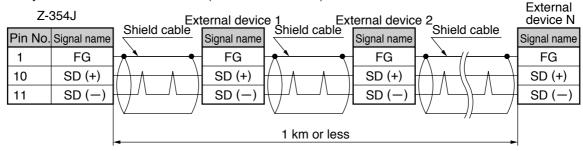
"1" by "N" connection is available. (Max. 15 sets for N)



This board needs not necessarily to be end station.

(4) RS-422A (2-wire system)

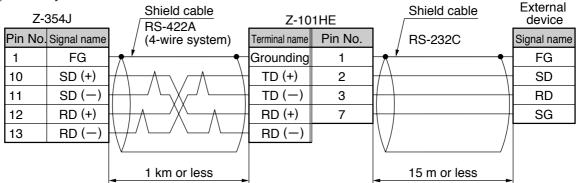
"1" by "N" connection is available. (Max. 15 sets for N)



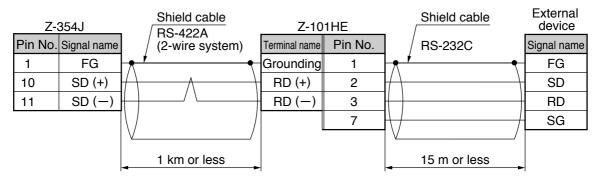
This board is not necessary be an end station.

(5) Using Z-101HE (RS-232C/RS-422A converter)

1) 4-wire system



2) 2-wire system automatic



• RS and CS of external device might be shorted in accordance with specification (available operation when both RS and CS are "ON") of its external device. Some external device cannot communicate unless DCD (carrier detection) is "ON."

In this case, latch DCD terminal signal at the external device side or loop back "ON" voltage signal.

• When Z-101HE automatic mode is applied, set transfer rate 2400 bits/s or up.

Notes				
* Be sure to use the following shielded twisted pair cables.				
Manufacturer	RS-232C, RS-422A (4-wire system)	RS-422A (2-wire system)		
HITACHI CABLE LTDCO -SPEV -SB0.5S -IREV -SW2*0.5				

Chapter 6: How to Use

Standard operation flow is shown below. Refer to this for use.

1. Turn OFF power supply to J -board.	Reference item (Pages to refer)
2. Set termination switch SW1.	─ → (6-2)
3. Set parameter area and control relay area by module No. switch.	Setting of parameter area, control relay area (6-3)
4. Set termination resistance switch	→ (4-2)
5. Check the switch settings	
SW2-1 = ON, SW2-2 = ON	
SW3 = 0	
SW6 = OFF	
Ļ	
6. Install the board together with a CPU board, etc.	→ Installation method (5-1)
· · · · · · · · · · · · · · · · · · ·	_
7. Connect external devices to the board.	→ Wiring method (5-2 to 4)
8. Turn ON the power supply to the board	
9. Set parameter.	Parameter setting (6-4 to 12)
10. Create program of J-board.	 Control relay (6-13 to 15) Program example (7-1, 2)
11. Enter the J-board in operation condition using a support tool.	
↓ 	
12. Start communication between the board and the external device.	

[1] Allocation of I/O relays

This board contains 8 bytes, as shown below.

I/O relays are allocated on this board as dummies. They do not function. However, please note that their assignment may affect I/O relay addresses on another I/O board.

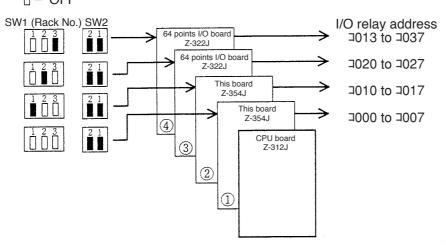
Allocation	I/O relay address	Actual address		
Dummy	000.⊏			
(vacant)	001. ב	R = 0, S = 0		
Dummy	⊐.002			
(vacant)	⊐.003	R = 0, S = 1		
Dummy	⊐.004			
(vacant)	⊐.005	R = 0, S = 2		
Dummy	006. ⊏	R = 0. S = 3		
(vacant)	⊐.007			

Allocation example when the rack No. is 0.

- Turn SW2-1 and SW2-2 ON

An example of the assignment of I/O relays when an I/O board is installed on a motherboard.



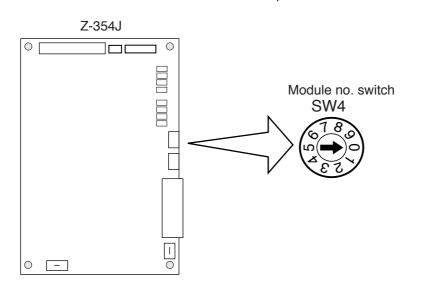


Remarks

- * When SW1 (rack no.) is set the to same number for multiple J-boards, they may have the same I/O allocations which can cause a malfunction.
- * When SW1 (rack no.) is turned ON on multiple stations, an "I/O verification error: Error code 60" will occur.

[2] Settings of parameter area, control relay area

Set parameter area to designate communication mode, transfer rate etc., and control relay area required for data sending/receiving programs using a module No. switch. Be sure not to set same number with other special I/O module.



Module No. switch	* Para	Control relay area	
setting value	Parameter address	Register address of file 1	(byte address)
0	T- 0 (000 to 177)	000000 to 000177	⊐0200 to ⊐0217
1	T- 1 (000 to 177)	000200 to 000377	⊐0220 to ⊐0237
2	T- 2 (000 to 177)	000400 to 000577	⊐0240 to ⊐0257
3	T- 3 (000 to 177)	000600 to 000777	⊐0260 to ⊐0277
4	T- 4 (000 to 177)	001000 to 001177	⊐0300 to ⊐0317
5	T- 5 (000 to 177)	001200 to 001377	⊐0320 to ⊐0337
6	T- 6 (000 to 177)	001400 to 001577	⊐0340 to ⊐0357
7	T- 7 (000 to 177)	001600 to 001777	⊐0360 to ⊐0377
8	- Prohibited to set		
9			

* Depending on a peripheral device used for entering parameters, addresses to enter vary. (=> Next page)

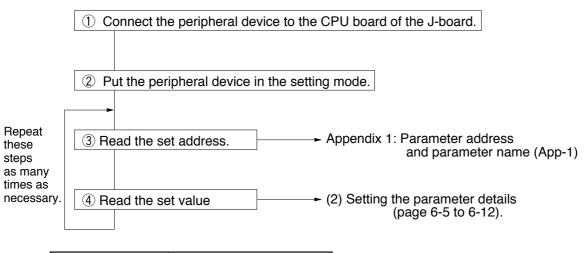
Remarks

* Only set the module no. switch when the power to the J-board is OFF. Use a slotted screwdriver.

[3] Parameter setting

Specify the parameters using a peripheral device. For details about the operation methods, see the manual for the peripheral device.

(1) Setting procedures



Peripheral device	Address to set	
JW-2PG	Parameters	
JW-10PG	File 1 register	
JW-11PG		
JW-12PG	Parameters	
JW-13PG		
ZW-101PG1	File 1 register	
Z-100LP2F		
JW-40PG	Specify the JW21/22	
JW-50PG	parameters for editing	
JW-92SP	programs	
JW-52SP	(Special I/O module)	

(Remarks)

When the power is turned ON or the J-board status changes from "stop" to "operating," the J-board's motherboard transfers the parameters to the J-board, and specifies the communication conditions.

(2) Setting the parameter details

The address shown below is parameter address. In case of register address of fiel 1, refer to appendix. 1.

Address T-0 to 7	Parameter name	Setting value (Hexadecimal)		Details	
000	Parameter transfer	22 (Initial value: 00) Execution of parameter transfer - Transfer parameters from CPU board's to the board - Other than 22 _{HEX} is treated as pramete error.		ameters from CPU board's memory	
		00 (Initial value)	Completion o	f BCC calculation	
001	BCC calculation	01	 Execution of BCC calculation At changing parameter, set to 01HEX. When the CPU board's changes from "programmode" to "date transfer mode" or it turns from OFF to ON, it calculates BCC codes of parameter setting contents and stores in parameter address 177. When the calculation is completed, set to 00HE 		
	Communi- cation mode	00 (Initial value)	RS-232C - Transfer mode is fixed to full-duplex.		
002		01 RS-422A (4-wire system) - Either full-duplex or half duplex is se transfer mode.		plex or half duplex is selectable as	
		02	RS-422A (2-wire system) - Transfer mode is fixed to half-duplex.		
003	00 (Initial value) Transfer		Full-duplex - Both sending time is possil - In case of RS	and receiving data at the same	
	mode	01		ding and receiving alternately. S-232C, it is fixed to full-duplex so ng is invalid.	
		00 (Initial value)	19200 bits/s		
		01	9600 bits/s		
004	Transfer	02	4800 bits/s- Select to match the external device's specifications		
	speed	03			
		04	1200 bits/s		
		05	600 bits/s		

Address T-0 to 7	Parameter name	Setting value (Hexadecimal)	Details		
		00 (Initial value)	 8 bits Data to be subject to JIS code, binary data, special character. 1 D0 D1 D2 D3 D4 D5 D6 D7 Parity 0 Start Data length Parity Stop bit (Ex.: 2 bits) 		
005	Data length	01	7 bits Data to be subject to ASCII code only. At communication with an external device the data length of which is fixed to 7 bits. 1		
006	Stop bit	00 (Initial value)	Select to match the external device's		
	-	01	1 bit specifications		
	Parity	00 (Initial value)	Even • Set and check that length of total amount of bits of data D_0 to D_7 (D_0 to D_6 at 7 bits data length) and "ON" bit of parity bit becomes even.		
007		01	 Odd Set and check that length of total amount of bits of data D₀ to D₇ (D₀ to D₆ at 7 bits data length) and "ON" bit of parity bit becomes odd. 		
		02	Absent Don't set and check above parity bit. 		

Address T-0 to 7	Parameter name	Setting value (Hexadecimal)	Details
		00 (Initial value)	Absent No check concerning communication. RS relay is normally ON.
010	Control signal	01	 RS/CS manual Communication mode: Valid at RS-232C. Control sending data by CS signal from external device. 1. When CS signal is 0, ready for data sending 2. When CS signal is 1, stop data sending. Sending data 1 2 3 4 5 6 7 →External device CS signal 0 → External device Control receiving data by RS relay. 1. When RS relay is OFF, RS signal becomes "1" and requests to stop data sending to an external device. 2. When RS relay is "ON," RS signal becomes "0" and requests to send data to an external device. 3. Receiving data time over error due to switch "OFF" RS relay does not occur.
	02	02	 RS/CS automatic Communication mode: Valid at RS-232C Control sending data by CS signal from external device. Contents is the same as RS/CS manual operation. Control receiving data by number of vacant data bytes of receiving buffer. When number of vacant data bytes becomes less than 16 bytes, RS signal turns to "1" and requests to stop sending data to an external device. When number of vacant data bytes becomes more than 17 bytes, RS signal turns to "0" and requests to send data to an external device. Number of vacant data bytes of

Remarks

* Match setting of control signal at external device side to this board's settings.

	Parameter	•	Details
T-0 to 7	name	(Hexadecimal)	
			 XON/XOFF manual Transfer mode: Valid at full-duplex. Control sending data by XON, XOFF codes from an external device. 1. Possible data sending by receiving XON [11_{HEX}]. 2. Data stops sending by receiving XOFF [13_{HEX}]. Sending data - 1 2 3 45 6 External device Receiving dataXOFFXON
010	Control signal		 Control receiving data by RS relay. 1. This board automatically sends XOFF [13_{HEX}] by turning RS relay from "ON" to "OFF," and requests to stop sending data to an external device. 2. This board automatically sends XON [11_{HEX}] by turning RS relay from "OFF" to "ON," and requests to send data to an external device. 3. Receiving data time over error by turning OFF RS relay does not occurs. RS relay ON - CPU board - External device Receiving data 1 2 3 4 - 5 6 + External device
		04	 XON/XOFF automatic Transfer mode: Valid at full-duplex. Control sending data by XON, XOFF codes from external device. Contents is as same as XON/XOFF manual operation. Control receiving data by number of vacant data bytes of receiving buffer. When number of vacant data bytes is less than 16 bytes, this board automatically sends XOFF [13_{HEX}] and requests to stop sending data to an external device. When number of vacant data bytes is more than 17 bytes, this board automatically sends XON [11_{HEX}] and requests to send data to an external device. When number of vacant device. When number of vacant data bytes is more than 17 bytes, this board automatically sends XON [11_{HEX}] and requests to send data to an external device.

Remarks

* Match setting of control signal at external device side to this board's settings.

Address T-0 to 7	Parameter name	Setting value (Hexadecimal)	Details
		00 (Initial value)	 Sending: Non-conversion Receiving: Non-conversion Send CPU board's data without conversion. Send data from an external device without conversion. JIS codes, binary data are usable (data length: 8 bits). CPU board data register Inside of sending/receiving buffer of this board Ist byte Sending data (+) External device * Max. value of n (2 points) is 512.
011	Transfer code conversion	01	 * Values in () means conditions at receiving. Sending: BIN -> ASCII conversion Receiving: Non-conversion Send data after convert BIN data of CPU board to ASCII form. (Sending header/terminators are sent without conversion.) Receive data from an external device without conversion. (At sending) CPU board data register MSB LSB Inside of sending buffer of the module byte

	Parameter	-	Contents		
T-0 to 7	name	(Hexadecimal)			
011	Transfer code conversion	02	Sending: Non-conversion Receiving: ASCII → BIN conversion • Send CPU board's data without conversion. • After converting data from an external device from ASCII to BIN, take as CPU board's data. • Only 0 to 9, A to F are usable as ASCII characters for receiving data (except receiving header, terminator). (At sending) Same as sending of setting value 00 _{HEX} . (See previous page) (At receiving) CPU board data register MSB LSB Inside receiving buffer of this board Inside receiving data • MSB LSB Inside receiving data • Inside receiving<		
		03	 Sending: BIN -> ASCII conversion Receiving: ASCII -> BIN conversion Send data after converting CPU board's data from BIN to ASCII. (Send header/terminators without conversion.) After converting data from an external device from ASCII to BIN, take as CPU board's data. Only 0 to 9, A to F are usable as ASCII characters for receiving data (except receiving header, terminator). (At sending) Same as sending of setting value 01_{HEX}. (See previous page) (At receiving) Same as receiving setting value 02_{HEX}. 		

Address T-0 to 7		Setting value (Hexadecimal)	Details			
		00	0 ms			
		(Initial value)	100 ms			
		01	200 ms			
	-	02	300 ms			
		04	400 ms			
		05	500 ms			
		06	600 ms	 While sending data, when data stops at 		
		07	700 ms	a middle of 1 data frame (see page 6-		
		08	800 ms	16), the timer starts.When this board does not send next data		
		09	900 ms	until the sending time over interval, a		
012	Sending	00 0A	1000 ms	sending time out error occurs. When this		
012	time over interval	0B	1100 ms	board sends next data, it resets the timer		
	intervar	0C	1200 ms	and continues sending.Allowance of setting time is 0 to +100		
		0D	1300 ms	ms.		
		0E	1400 ms	(Example)		
		0F	1500 ms	At 00_{HEX} setting: 0 ms to 100 ms At 01_{HEX} setting: 100 ms to 200 ms		
	-	10	1600 ms	At 0THEX Setting. Too his to 200 his		
		11	1700 ms			
		12	1800 ms			
		13	1900 ms			
		14	2000 ms			
		Other than above	Setting prohibited			
		00 (Initial value)	0 ms			
		01	100 ms			
		02	200 ms			
		03	300 ms			
		04	400 ms	 While the board is receiving data, if the 		
		05	500 ms	receiving data stops sending at a middle		
		06	600 ms	of 1 data frame (see page 6-17), the timer starts counting.		
		07	700 ms	 If the board does not receive the next 		
		08	800 ms	data within the receive time out interval,		
	Receiving	09	900 ms	the receive time out error occurs. If the board receives the next data within the		
013	time over	0A	1000 ms	specified time interval, the board resets		
	interval	0B	1100 ms	the timer and continues receiving data.		
		0C	1200 ms	Allowance of the specified time is the		
		0D	1300 ms	same as the sending time out time.When the RS/CS manual or XON/XOFF		
		0E	1400 ms	manual is selected as control signal, and		
		0F	1500 ms	if the RS relay is OFF, the receiving time		
		10	1600 ms	over error does not occur.		
		11	1700 ms			
		12	1800 ms			
		13	1900 ms			
		14	2000 ms			
		Other than above	Setting prohibited			

Address T-0 to 7	Parameter name	Setting value (Hexadecimal)	Details		
014 015 016 017	EXP1 header	(Initial value: 00)	 When EXP1 header, EXP1 terminator are set at sending header/terminator or receiving header/terminator of a control relay (see page 6-14), any set code in this parameter is valid as control character. When NUL code [00_{HEX}] is set, thereafter set code becomes invalid so that header can use 1 to 4 characters and terminator can use 1 to 2 characters. When NUL code is set at each top address 014, 020, it becomes header: absent, terminator: absent, conditions. 		
020 021	EXP1 terminator		Address Setting value Character 014 $3A_{(H)}$: 015 $3F_{(H)}$? 016 $00_{(H)}$ NUL 017 $00_{(H)}$ NUL 020 $40_{(H)}$ @ 021 $00_{(H)}$ NUL — Invalid $+$ Header Terminator (1 char.)		
022 023 024 025	EXP2 header	(Initial value: 00)	• When EXP2 header, EXP2 terminator are set at sending header/terminator or receiving header/terminator of a control relay (see page 6-14), any set code in this parameter is valid as control character.		
026 027	EXP2 terminator		 Setting contents is as same as EXP1 header, EXP1 termina- tor. 		
030 to 176	Not used	(Initial value: 00)			
177	BCC code		 Setting is not required. (Setting by support tool is invalid). Store check code of parameter. (This board checks parameter setting values from CPU board using this code.) 		

[4] Control relay

Control relays are used for a program to send and receive data. As for programming, refer to the basic program on "Chapter 7: Program Example."

(1) Control relay contents

Addresses shown below are true when the module No. switch setting is "0." For settings of other numbers, refer to appendix 2.

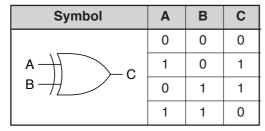
Address		Name of control relay	
	D ₁	T _{RDY} (Ready sending operation)	 Turns "ON" when data sending to an external device is available. When TRDY relay turns from OFF" to "ON," this board clears error codes of control relay and recovers from error condition.
⊐0200 -	D ₃	R _{REQ} (Request to transfer receiving data)	 Turns "ON" when the CPU board requests to send receiving data to this board. While R_{REQ} relay turns from "OFF" to "ON," when this board has received data, it turns "ON" RACK relay. (It also clears error codes.)
	D ₅	RS (Request to send)	 Control RS (control signal of RS-232C) from the CPU board, this signal turns "ON" when requesting to send data to an external device. (RS signal voltage is +10 V.) Valid when parameter setting of control signal (Page 6-7 and 6-8) is "RS/CS manual" and "XON/XOFF manual."
C	D ₁	TਸeQ (Request to transfer sending data)	- Turns "ON" when there is no data in sending buffer of this board.
	D ₃	R _{RDY} (Ready to transfer receiving data)	- Turns "ON" when this board has receiving data, and ready to transfer to the CPU board.
⊐0201	D ₄	R _{аск} (Ready to transfer receiving data)	 Turns "ON" when this board has receiving data while RREQ turns from "OFF" to "ON." This board transfers receiving data to the CPU board by F-85 instruction while RACK turns from "OFF" to "ON," and turns "OFF" after completion of transfer.
	D ₅	CS (Ready sending)	 Monitors CS (control signal of RS-232C) at the CPU board, and turns "ON" when an external device is ready to receive data. (CS signal voltage is +3 to +15 V.)
	D ₇	URDY (Available operation of this board)	 This board turns "ON" this signal when it ready to operate, and keeps "ON" condition with both of the below conditions. Completion of transfer parameter from the CPU board to this board. Mode switch is set to "0."
0202		Error code (See page 8-1 and 8- 2.)	 This board converts errors which occurred inside to error codes, and outputs to this address. Error code becomes 00_{HEX} (normal operation) at following conditions. When T_{RDY} relay turns from "OFF" to "ON." J-board starts operation from stopped condition. When more than one errors occurs, this board outputs a priority error code.

Add	ress	Name of control relay	Setting value or output value		Details	;	
				Receiving header	Receiving terminator	Specify these add-	
			O _{HEX}	Absent	Absent	resses in order to determine whether the	
			1 _{HEX} *1	EXP1 header	EXP1 terminator	data received are	
	Do	Dessiving	2 _{HEX} *1	EXP2 header	EXP2 terminator	intended for this board	
	to	Receiving header/	3 _{HEX}	Absent	CR	or not. (Receiving data)	
	D ₃	terminator	4 _{HEX}	Absent	LF		
			5 _{HEX}	Absent	CR • LF	Receiving header Receiving terminator	
			6 _{HEX}	STX	ETX	seiving Data	
			7 _{HEX} *2	STX	ETX + BCC (1 byte)	La L	
20202			Other than above	Setting p	prohibited	Top data End data	
⊐0203				Receiving header	Receiving terminator	Specify these add-	
			O _{HEX}	Absent	Absent	resses in order to determine whether the	
			1 _{HEX} *1	EXP1 header	EXP1 terminator	data sent are intended	
	D4	Senaing	2 _{HEX} *1	EXP2 header	EXP2 terminator	for this board or not.	
	to		3 _{HEX}	Absent	CR	(Sending data)	
	D_7		4 _{HEX}	Absent	LF		
				5 _{HEX}	Absent	CR • LF	Sending header Data Sending terminator
			6 _{HEX}	STX	ETX	ading t	
			7 _{HEX} *2	STX	ETX + BCC (1 byte)	Ser la se	
			Other than above	Setting p	prohibited	Top data End data	
⊐02 (Lov			0000 _{HEX}	Variable data length	 Set number of byte of sending data to external device. When number of byte is set to "varial 		
			0001 _{HEX}	1 byte	data length," the	module is unable to ng terminator is "ab-	
⊐02 (Up		No. of sending bytes	to	to	sent," and automatically calculates number of sending bytes while sending terminator is "present." • As for relationship with other settings, see page 6-16.		
	,		0200 _{HEX}	512 bytes			
			Other than above	Setting prohibited			
⊐02	206		0000 _{HEX}	0 byte	Setting is not recently a setting is not recently a setting is not recently a set of the set o		
(Lov	ver) 207	No. of transfer bytes	to	to	data bytes from (except receivi	g number of receiving n an external device ng header/terminator), puts the result to this	
	(Upper)		0200 _{HEX}	512 bytes	 As for relationsh see page 6-17. 	ip with other settings,	
			0000 _{HEX}	0 byte		ytes of receiving data	
⊐02			to	to	from an external		
(Lov	,	No. of receiving	0200 _{HEX}	512 bytes		r/terminator are "ab-	
]02 (Up)		bytes	Other than above	Setting prohibited		ip with other settings,	

- *1 (4 places) Become parameter set code (see page 6-12.)
- *2 (2 places) How to make a BCC code

BCC check is made based on the calculation of the range from STX onward up to ETX. The calculations are all performed in bit patterns of ASCII 7-bit codes.

1) Operate XOR of the first character and the second character of the communication data. Truth table of eXclusive OR



- 2) Operate XOR of the result of that operation and the third character.
- 3) Determine the result of operation sequentially and, lastly, operate XOR of ETX to take it as BCC code.

(Examp	ple)		
	1st 2nd word word ······		8th word
STX	31 32 33	34	35 36 37 38 ETX BCC
	Calculat	ed area	of BCC check
ASCII	Binary value		XOR value
31	110001 —		,
32	110010	\oplus	110001=Result (1)
33	110011	\oplus	000011=
34	110100	\oplus	110000=
35	110101	\oplus	000100=
36	110110	\oplus	110001=
37	110111	\oplus	000111=
38	111000	\oplus	110000=
ETX(03)) 000011	\oplus	001000=
			001011 BCC code

Remarks

* The number of transfer bytes and receiving bytes are values after converting transfer code at data receiving. Therefore, when this board receives after conversion from ASCII to BIN (page 6-10), number of data bytes at an external device is 1024 bytes at max.

Number of transfer bytes,	Number of data bytes at the external device							
number of receiving bytes	Receiving: non-conversion	Receiving: ASCII -> BIN conversion						
1	1	2						
2	2	4						
to	to	to						
511	511	1022						
512	512	1024						

[5] Sending data

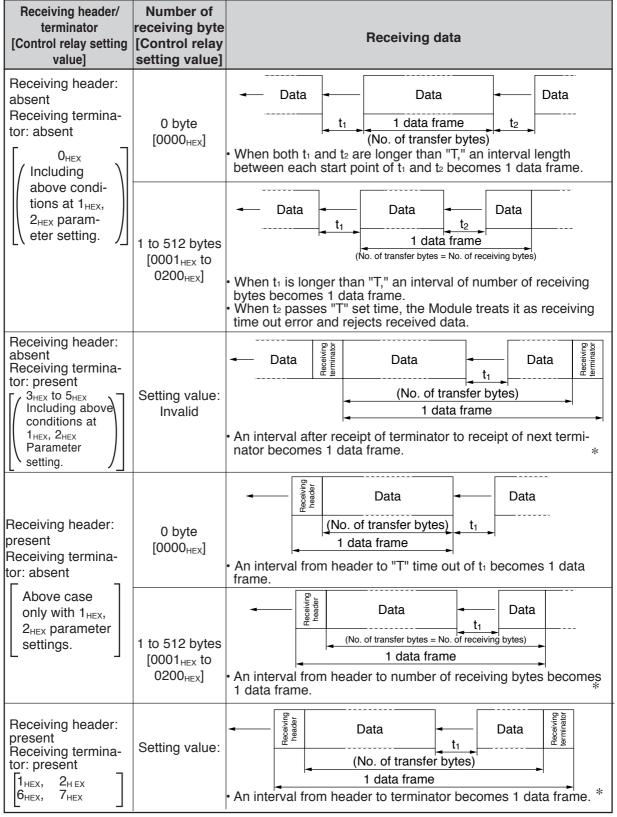
Relations between sending header/terminator, number of sending bytes (P6-14) and 1 data frame of sending data are as follows:

Data sending terminator [Control relay setting value]	Number of sending byte [Control relay setting value]	Sending data				
Absent	Variable data length [0000 _{HEX}]	 Unable sending (Number of sending bytes uncertain error) 				
0 _{HEX} Including setting EXP1 terminator or EXP2 terminator of parameter to "absent" at 1 _{HEX} , 2 _{HEX} settings.	1 to 512 bytes [0001 _{нех} to 0200 _{нех}]	Data from CPU board (No. of sending byte) Sending Data Top data 1 data frame Add sending header set by control relay automatically.				
	Variable data length [0000 _{HEX}]	 Data from CPU board (No. of sending byte) Sending header Data Sending terminator Top data 1 data frame Calculate the number of sending bytes automatically. Setting sending header/terminator set in the CPU board data with control relay is required. 				
Present [1 _{HEX} to 7 _{HEX}]	1 to 512 bytes [0001 _{нех} to 0200 _{нех}]	 Data from CPU board (No. of sending byte) Sending header Data Sending terminator Top data 1 data frame Add sending header/terminator set by control relay automatically. 				

[6] Receiving data

Relationship between receiving header/terminator, number of receiving bytes (page 6-14), receiving time out interval (page 6-11), number of transfer bytes (page 6-14) and 1 data frame of receiving data are as follows:

(t₁, t₂ are intervals between each receiving data, "T" as receiving time out interval.)



* (3 places) When t₁ exceeds T, it becomes receiving time out error and rejects received data.

(Remarks)

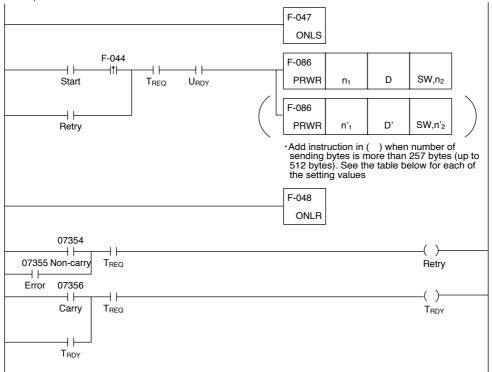
* When number of transfer bytes exceeds 512, it becomes overflow error and rejects received data.

Chapter 7. Program Example

This chapter describes basic program of data sending and data receiving, and its application examples. Use this basic program for programming.

(1) Data sending

When the start relay turns from "OFF" to "ON" while there is no sending data in the module sending buffer, the module transfers J-board's data to the module's sending buffer by F-86 instruction, and starts data send to an external device. The retry relay function is capable of re-transferring data which was not executed data transfer by F-86 instruction. (As for functions of each control relay, see page 6-13.)



• Carry flag (07356) turns ON when only the data transfer is completed using F-86 instruction.

1) Set the relay number

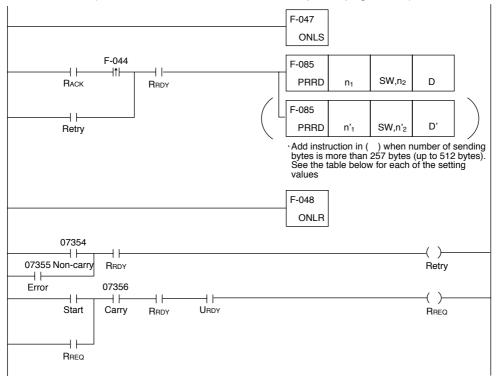
Start, retry --- Any number

- TRDY, TREQ, URDY --- The number set with the module no. switch on this board (see App-2).
- 2) Setting value of F-86 instruction

	Number	of sending bytes						
	0 to 256	257 to 512						
nı (transfer bytes)	Set 001oct to 377oct for 0 to 255, and 000oct for 256 of number of sending bytes.	000ост						
D (top address)	Set the top address of sending data ⊐0000 to ⊐1577, b0000 to b1777, 09000 to 99777, E0000 to E1777							
SW (switch setting)	Specify the module number switch v	value of this board (0 to 7).						
n2 (transfer data)	0	0						
n'1 (number of transfer bytes)		Set 001oct to 377oct for 257 to 511, and 000oct for 512 of number of transfer bytes.						
D' (top address)		Set the 257th byte address counting from address D.						
n'2 (transfer area)		1						

(2) Data receiving

When start relay turns from "OFF" to "ON" while there is data from an external device in the module sending buffer, the module transfers data in receiving buffer to data register of J-board instruction. The retry relay function is capable of re-transferring data which was not executed data transfer by F-85 instruction. (As for functions of each control relay, see page 6-13.)



• Carry flag (07356) turns ON when only the data transfer is completed using F-85 instruction.

1) Set the relay number

Start, retry --- Any number

RACK, RRDY, RREQ, URDY --- The number set with the module no. switch on this board (see App-2). **2) Setting value of F-85 instruction**

	Number of s	sending bytes						
	0 to 256	257 to 512						
nı (transfer bytes)	Set 001oct to 377oct for 0 to 255, and 000oct for 256 of number of receiving bytes.	000 ост						
D (top address)	Set the top address which transfer the receiving data ⊐0000 to ⊐1577, b0000 to b1777, 09000 to 99777, E0000 to E1777							
SW (switch setting)	Specify the module No. switch value of this board (0 to 7).							
n2 (transfer data)	2	2						
n'ı (number of transfer bytes)		Set 001oct to 377oct for 257 to 511, and 000oct for 512 of number of transfer bytes.						
D' (top address)	Set the 257th byte address countin address D.							
n'2 (transfer area)		3						

Chapter 8. Error and Treatment

When this board is abnormal, EA, EB, or FT on the LED lights (see page 4-1), and stores error code in control relay area. (See page 6-13 and App-2.)

The stored address is fixed by module No. switch setting of the module.

Module no. switch specified	0	1	2	3	4	5	6	7
Error code storage address	⊐0202	⊐ 0222	⊐0242	⊐0262	⊐0302	⊐0322	⊐0342	⊐0362

Causes and treatments for errors

	Error code (Hexa- decimal)	Name	Cause	Treatment [() means page to refer.]	Priority	Lighting LED
	00	Normal operation			_	—
	80	Parameter transfer error	Set other than 22_{HEX} in parameter transfer.	Check setting value of param- eter transfer (6-5)	2	
	81	BCC calculation error	Setting value of BCC calculation is incorrect.	Check setting value of BCC value. (6-5)	13	
	82	Communication mode error	Setting value of communication mode is outside the range.	Check setting value of commu- nication mode (6-5)	3	
	83	Transfer mode error	Setting value of transfer mode is outside the range.	4		
error	84	Transfer rate error	Setting value of transfer rate is outside the range.	5		
setting er	85	Data length error	Setting value of data length is outside the range.	Check setting value of data length. (6-6)	6	EA
eter se	86	Stop bit error	Setting value of stop bit is outside the range.	Check setting value of stop bit. (6-6)	7	
Parameter	87	Parity error	Setting value of parity is outside the range.	Check setting value of parity. (6-6)	8	
[88	Control signal error	Setting value of control signal is outside the range.	Check setting value of control signal. (6-7 to 6-8)	9	
	89	Transfer code conversion error	Setting value of transfer code conversion is outside the range.	Check setting value of transfer code conversion. (6-9 to 6-10)	10	
	8A	Sending time over interval error	Setting value of sending time over interval is outside the range.	Check setting value of sending time over interval. (6-11)	11	
	8B	Receiving time over interval error	Setting value of receiving time over interval is outside the	Check setting value of receiv- ing time over interval. (6-11)	12	

	Error code (Hexa- decimal)	Name	Cause	Treatment	Priority	Lighting LED
	90	Sending, receiving header/terminator error	Setting value of sending, receiving header/terminator is outside the range.	Check control relay settings of sending, receiving header/ terminator. (6-14)	14	
setting error	91	Number of sending bytes error	 Setting value of the number of sending bytes is outside the range. Set sending header/terminator to "absent," and number of sending byte setting is "variable data length." 	Check number of sending byte setting control relay, sending header/terminator setting control relay. (6-14)	15	EA
Control relay setting	92	Number of sending bytes undefined error	While number of sending bytes being "variable data length," there is no sending terminator in sending data.	Check sending data, sending header/terminator setting control relay, number of sending byte setting control relay, sending header/ terminator setting parameter. (6-12, 6-14)	17	
	93	Number of receiving bytes error	Setting value of number of receiving byte is outside the range.	Check number of receiving byte setting control relay. (6- 14)	16	
	AO	Parity error	An error occurs by parity check during receiving data.	 Check communication setting with external device. Check external device, communication cable. 	19	
	A1	Framing error	Receiving data is abnormal.	 Check communication setting with external device. Check external device, communication cable. 	20	
	A2	Overrun error	Receiving data exceeds receiving buffer capacity.	Check number of sending data of external device.	21	
Communication error	A3	Overflow error	Number of receiving bytes exceeds 512 bytes which is a limit to transfer to CPU board.	Check number of sending data of external device.	22	ЕВ
ommunica	A4	Data conversion error	Receive unable to convert code by ASCII -> BIN conversion of receiving data.	Check sending data of external device.	23	
ŏ	A5	BCC check error	When receiving header/ terminator is set to STX-ETX + BCC, BCC code calculated by receiving data differs from received BCC codes.	Check sending data of external device.	24	
	A6	Sending time over error	Prohibition to receive of an external device continued longer than sending time out interval set in parameter.	Check external device, communication cable.	26	
	A7	Receiving time over error	Data stopped condition while data receiving continued longer than receiving time out interval set in parameter.	Check external device, communication cable.	25	-
Hardware error	CO	Hardware error	Error occurs by ROM sum check, RAM read/write check inside the module.	Replace this board.	1	EA
Communica- tion error	C1	Data collision error	When the transfer mode is half duplex, both the module and an external device send data, or communication cable is shorted.	 Check sending, receiving timing with external device. Check communication cable. 	18	EB
		Module error	Watchdog timer timed over.	Replace the module	_	FT
		SM3 error	SM3 is set to other than 0.	Set SM3 to 0.	_	EA EB

Chapter 9: Specifications

(1) Generaleral specifications

Items	Specifications
Ambient operation temperature	0 to +55°C/35 to 90% (without dew condensation)
Storage temperature /humidity	-20 to +70°C/35 to 90 %RH (without dew condensation)
Vibration	JIS C0911 or equivalent Duplex width: 0.15 mm (10 to 55 Hz), 1 G (55 to 150 Hz), 2 hours each in the X, Y, and Z directions
Shock	JIS C0912 or equivalent. 10 G 3 times in X, Y, and Z directions
Noise immunity	1000 Vp-p, 1μ s (by noise simulator: Voltage charged between the 24 VDC line (pin 9) and the FG on the motherboard.)
Installation direction	Any of the 3 directions.
Grounding	Class 3 grounding
Accessories	4 screws (Semuth M3 x 6 mm), 4 standoffs (15 mm)

(2) Performance specifications

Item	Specifications
Number of I/O points	I/O relays: 64 points (dummies) Data relay: 128 points Parameter settings: 128 bytes
Internal current consumption	210 mA
Weight	Approx. 150 g

(3) Communication specifications

Items	Specifications						
Items	RS -232C	RS -422A					
Number of serial I/F ports	1 (Use either RS-232C or RS-422A)						
Specifications	No protocol						
Number of connectable modules	1 set	Max. 15 sets					
Data transfer standard	EIA RS-232C	EIA RS-422A (2-wire system, 4-wire system)					
Transfer rate	600, 1200, 2400, 4800, 9600, 19	200 bits/sec.					
Synchronous mode	Start-stop system	Start-stop system					
Transfer mode	Full-duplex/half-duplex						
Circuit configuration	1:1	1 : N					
Control signal	Absent, RS/CS manual, RS/CS a XON/XOFF automatic	automatic, XON/XOFF manual,					
Data length	7/8 bits						
Parity	Absent, odd, even						
Stop bit	1/2 bits						
Control character	Absent, EXP1, EXP2, CR, LF, CR - LF, STX, ETX, ETX+BCC						
Transfer code	Non-conversion, BIN <-> ASCII conversion						
Number of sending/receiving bytes	^{ring} Individual setting for sending/receiving (1 to 512 bytes)						
Communication line	Cable total length :Max. 15 mCable total length :Max(Shielded twisted pair cable)(Shielded twisted pair cable)						

Appendix

Appendix 1. Parameter address and parameter name

The below table shows the relation between address of parameter area and name of parameter set by module No. switches.

Parameter address		(Se	•		Iress of nodule N		ch)		Parameter name
T-0 to 7	0	1	2	3	4	5	6	7	
000	000000	000200	000400	000600	001000	001200	001400	001600	Parameter transfer
001	000001	000201	000401	000601	001001	001201	001401	001601	BCC calculation
002	000002	000202	000402	000602	001002	001202	001402	001602	Communication mode
003	000003	000203	000403	000603	001003	001203	001403	001603	Transfer mode
004	000004	000204	000404	000604	001004	001204	001404	001604	Transfer rate
005	000005	000205	000405	000605	001005	001205	001405	001605	Data length
006	000006	000206	000406	000606	001006	001206	001406	001606	Stop bit
007	000007	000207	000407	000607	001007	001207	001407	001607	Parity
010	000010	000210	000410	000610	001010	001210	001410	001610	Control signal
011	000011	000211	000411	000611	001011	001211	001411	001611	Transfer code conversion
012	000012	000212	000412	000612	001012	001212	01412	001612	Sending time over interval
013	000013	000213	000413	000613	001013	001213	001413	001613	Receiving time over interval
014	000014	000214	000414	000614	001014	001214	001414	001614	
015	000015	000215	000415	000615	001015	001215	001415	001615	
016	000016	000216	000416	000616	001016	001216	001416	001616	EXP1 header
017	000017	000217	000417	000617	001017	001217	001417	001617	
020	000020	000220	000420	000620	001020	001220	001420	001620	
021	000021	000221	000421	000621	001021	001221	001421	001621	EXP1 terminator
022	000022	000222	000422	000622	001022	001222	001422	001622	
023	000023	000223	000423	000623	001023	001223	001423	001623	
024	000024	000224	000424	000624	001024	001224	001424	001624	EXP2 header
025	000025	000225	000425	000625	001025	001225	001425	001625	
026	000026	000226	000426	000626	001026	001226	001426	001626	
027	000027	000227	000427	000627	001027	001227	001427	001627	EXP2 terminator
030	000030	000230	000430	000630	001030	001230	001430	001630	
to 176	to	to	to	to	to 001176	to	to	to	Not used
170								001778	BCC code
	000177	000377	000377	000777	001177	001377	001577	001/1/	

Appendix 2. Address and name of control relay

The below table shows the relation between address of control relay area and name of control relay set by module No. switches.

Control relay address (Setting value of module No. switches)						Name of control relay (Bit address)						Signal				
0	1	2	3	4	5	6	7	D ₇	D ₆	D₅	D ₄	D ₃	D ₂	D ₁	D ₀	direction
⊐0200	⊐0220	⊐0240	⊐0260	⊐0300	⊐0320	⊐0340	⊐0360			RS		R_{REQ}		T _{RDY}		CPU board -> Z-354J
⊐0201	⊐0221	⊐0241	⊐0261	⊐0301	⊐0321	⊐0341	⊐0361	URDY		CS	RACK	R _{RDY}		T _{REQ}		CPU board <- Z-354J
⊐0202	⊐0222	⊐0242	⊐0262	⊐0302	⊐0322	⊐0342	⊐0362				Error	code				
⊐0203	⊐0223	⊐0243	⊐0263	⊐0303	⊐0323	⊐0343	⊐0363	Sendir	ng head	der/tern	ninator	Re	eceivin	g heade	ər/	
⊐0204	⊐0224	⊐0244	⊐0264	⊐0304	⊐0324	⊐0344	⊐0364		Ν	lumber	of sen	ding by	tes (Lo	ower)		CPU board -> Z-354J
⊐0205	⊐0225	⊐0245	⊐0265	⊐0305	⊐0325	⊐0345	⊐0365		Ν	lumber	of rece	eiving b	ytes (L	Jpper)		
⊐0206	⊐0226	⊐0246	⊐0266	⊐0306	⊐0326	⊐0346	⊐0366		Ν	lumber	of tran	sfer by	tes (Lo	wer)		CPU board <- Z-354J
⊐0207	⊐0227	⊐0247	⊐0267	⊐0307	⊐0327	⊐0347	⊐0367		Ν	lumber	of tran	sfer by	tes (Up	oper)		
⊐0210	⊐0230	⊐0250	⊐0270	⊐0310	⊐0330	⊐0350	⊐0370	Number of receiving bytes (Lower)						CPU board -> Z-354J		
⊐0211	⊐0231	⊐0251	⊐0271	⊐0311	⊐0331	⊐0351	⊐0371	Number of receiving bytes (Upper)								
⊐0212	⊐0232	⊐0252	⊐0272	⊐0312	⊐0332	⊐0352	⊐0372									
to ⊐0217	to ⊐0237	to ⊐0257	to ⊐0277	to ⊐0317	to ⊐0337	to ⊐0357	to ⊐0377				Not ı	used				

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